

L Number	Hits	Search Text	DB	Time stamp
1	19	("3681578" "4314349" "4365318" "4523313" "4587481" "4698807" "4802119" "4802164" "4823256" "4891810" "4958273" "5042008" "5045995" "5051994" "5136498" "5423024" "5440724" "5504670" "5596759").PN.	USPAT	2004/09/17 13:23
2	2	6067633.URPN.	USPAT	2004/09/17 13:29
3	19	("3681578" "4314349" "4365318" "4523313" "4587481" "4698807" "4802119" "4802164" "4823256" "4891810" "4958273" "5042008" "5045995" "5051994" "5136498" "5423024" "5440724" "5504670" "5596759").PN.	USPAT	2004/09/17 13:47
-	178	710/7.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 15:28
-	230	714/31.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 15:40
-	761	714/11,13.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 15:54
-	18	system adj on adj a adj chip	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 08:41
-	0	fpga same multiprocessor same crossbar same redundan\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 08:34
-	1	fpga and multiprocessor same crossbar and (redundan\$2 near3 processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 08:34
-	6	switch\$3 and multiprocessor same crossbar and (redundan\$2 near3 processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 08:35
-	38	switch\$3 and (multiprocessor same (crossbar fpga switch\$3)) and ((redundan\$2 same (processor cpu)) and ("same" adj (device chip circuit board)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 08:40
-	18	systems adj on adj a adj chip	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 08:42
-	170	"systems-on-a-chip"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 08:42
-	1	"systems-on-a-chip" same (redundan\$2) and (failover recovery backup)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 08:52

-	785	multiprocessor same ((redundan\$2) or (failover recovery backup))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 09:01
-	492	(multiprocessor same ((redundan\$2) or (failover recovery backup))) and redundan\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 08:55
-	24	(multiprocessor same ((redundan\$2) or (failover recovery backup))) and redundan\$2 and failover	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 09:01
-	558	vlsi same ((redundan\$2) or (failover recovery backup))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 09:01
-	0	(vlsi same ((redundan\$2) or (failover recovery backup))) and failover and redundanc\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 09:02
-	0	(vlsi same ((redundan\$2) or (failover recovery backup))) and failover	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 09:02
-	16	vlsi and failover	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 09:07
-	12152	(single adj (chip circuit semiconductor)) near10 (multiprocess\$3 (multi\$3 process\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 09:08
-	6672	(single adj (chip circuit semiconductor)) near3 (multiprocess\$3 (multi\$3 process\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 09:08
-	5	((single adj (chip circuit semiconductor)) near3 (multiprocess\$3 (multi\$3 process\$3))) and failover	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 09:09
-	168529	high\$2 adj (reliab\$5 avail\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 09:11
-	196	((single adj (chip circuit semiconductor)) near3 (multiprocess\$3 (multi\$3 process\$3))) and fault adj (recover\$2 toleran\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 09:11
-	2	5285381.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 12:16
-	4	("4392199" "4438494" "4442502" "4622667").PN.	USPAT	2004/09/17 12:17